

# FIRSTNAME I. LASTNAME

[firstname.lastname.2018@gmail.com](mailto:firstname.lastname.2018@gmail.com)

Address - City - State - Zip Code

(212) 555-1212

## SENIOR HARDWARE DESIGN ENGINEER

---

Senior Hardware Design Engineer • Senior Systems Engineer • Staff Engineer • Principal Engineer  
Analog & Digital Design • Microprocessor/SOC Hardware • Schematic Capture • Board Layout  
Led System Level Design • Firmware Expertise • Strong Communicator • ODM Experience  
S.T.A.R Award 2018 • Employee of the Year 2017 • 3x Patents • Promoted Early

## WORK EXPERIENCE

---

**Company Name, Inc.,** New York, NY  
**Senior Hardware Design Engineer**

**Jan 2015 - present**

*Developer & Provider of Technology Solutions*

- Generated BOMs for designs with over xxx components...
- Optimized xx hardware prototypes by redesigning core...
- Oversaw xx firmware development projects to improve...
- Introduced designs for xx fixtures to increase production of...
- Delivered \$xxx in immediate cost-savings by testing mission...
- Increased process efficiency by xx% by introducing a database...
- Achieved x.x% accuracy of target mixture setting in design of...
- Improved revision documentations with xx+ page schematics...

**Prior Company, Inc.,** New York, NY  
**Senior Hardware Design Engineer**

**Jan 2011 - Jan 2015**

*Semiconductor and Telecommunications Company*

- Reduced product complexity by xx%, replacing x PCB boards...
- Lowered Hardware Manufacturing Cost by xx%....
- Achieved xx% efficiency rise overseeing testing and verification...
- Introduced design for a high-performance DMA controller to win...
- Increased TTM for \$xM product line by developing test procedure...
- Oversaw designs of xx component parts to help speed approval...
- Maximized capacity of circuit simulator to more than x times...
- Devised and produced processor design with xxx+ circuit elements...

**Prior Company, Inc.,** New York, NY

2004 - 2011

*International Technology & Software Provider*

**Senior Hardware Design Engineer**

**Jan 2010 - Jan 2011**

- Saved approx. \$xxx by improving chip design process over...
- Optimized efficiency of xx-strong team to bring approval...
- Introduced methodology for matrix solving with xx% increase...
- Delivered on ambitious TTM for \$xM contract by overseeing...

**Hardware Design Engineer**

**Jan 2004 - Jan 2010**

*Promoted From Hardware Design Engineer to Senior Hardware Design Engineer*

- Reduced timing closure turn-around time by x% by pioneering...
- Increased client data capture and processing capacity by xx%...
- Saved \$xM for client by developing an IP centric information...
- Contributed to prevention of a major network outage for client...

**Prior Company, Inc.,** New York, NY

**Jan 2001 - Jan 2004**

**Hardware Design Engineer**

*Manufacturer of Security Systems*

- Improved infrastructure design plans by introducing...
- Delivered cost reduction of xx% by changing designs in...

## **EDUCATION**

**Grad School University Name**

**2001**

**Master's in Precisely Specific Degree**

- Winner of award, distinction or honor
- Winner of award, distinction or honor

**Undergraduate College or University Name**

**1999**

**BS/BA in Precisely Specific Degree**

- Winner of award, distinction or honor

## **OPTIONAL PERSONAL, AWARDS, PATENTS, TECHNOLOGIES, KEYWORDS**

---

S.T.A.R Award 2018, Employee of the Year 2017, 3x Patents (Design), Cisco Certified Network Professional (CCNP), Distributed Generation Certified Professional (DGCP), Microsoft Certified Solutions Expert (MCSE), Certified Computer Security Incident Handler (CSIH), DDR2/3, Ethernet, USB, SATA, 12C SPI, UART, PCB, ASIC/FPGA, HW/FW/SW, Visual Basic, Altium Designer, Orcad, Cadence Allegro, CADSTAR, CircuitLogix, CircuitMaker, Advanced Design System, Active-HDL, CR-5000, DipTrace, DesignSpark PCB, EAGLE, EasyEDA, gEDA

*Firstname I. Lastname*